

ABSTRACT OF THE DISCLOSURE

Disclosed is a serial data mapping apparatus and method for mapping DS serial data into VC parallel data which includes an STM-1 address generating unit for generating a mapping address; a VC mapping unit for mapping a DS asynchronous signal into a VC signal as byte unit, according to the mapping address; and an STM-1 formatting unit for pointer processing and multiplexing a virtual container of the mapped VC signal and generating an STM-1 signal.

Disclosed is a serial data mapping apparatus and method for mapping DS serial data into VC parallel data which includes an STM-1 address generating unit for generating a mapping address; a VC mapping unit for mapping a DS asynchronous signal into a VC signal as byte unit, according to the mapping address; and an STM-1 formatting unit for pointer processing and multiplexing a virtual container of the mapped VC signal and generating an STM-1 signal.